



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of)
SHIMAMOTO et al.)
Application Number: 10/020,314) Art Unit 2823
Filed: December 18, 2001) Examiner
For: FABRICATING METHOD OF)
SEMICONDUCTOR INTEGRATED CIRCUITS)
Attorney Docket No. HITA.0143)

**Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450**

PETITION FOR EXTENSION OF TIME

Sir:

Applicants request that the time for taking action in this case be extended pursuant to 37 C.F.R. § 1.136(a) for **two (2) months**. The fee set in 37 C.F.R. § 1.17 for the extension of time is **\$450.00**.

Please charge any additional fee required for this extension of time to Deposit Account 08-1480. A duplicate copy of this paper is enclosed.

Respectfully submitted,

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March 28, 2007